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Hwa-Ya Technology Park 669, Fuhsing 3 Rd., Kueishan, Tao-Yuan Hsien, Taiwan, R.O.C.						
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STATEMENT UNDER 37 CFR 3.73(b)
Applicant/Patent Owner: Hsiu-Chun Lee, Tse-Yao Huang, Yi-Nan Chen
Application No./Patent No.: US 6,974,741 B2 Filed/Issue Date: 12/13/2005
Entitled: Method for forming shallow trench in semiconductor device
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